**SECTION 5.5**

**GIVING INSTRUCTION THAT MAY HAVE TO BE EXECUTED**

if00 [matc[$InfoXXYZ-[cs-s|sc-s], $InfoXXYZ-[cs-s|sc-s]]]: instruction

|| ifmt vs ifmt | ifmt vs stream | stream vs ifmt | stream vs stream

|| matc: exists between | diff: not exist between

|| Only for stream(s) and free (not for typed)

|| || ++ []

if00 [diff[$InfoXXYZ-[cs-s|sc-s], $InfoXXYZ-[cs-s|sc-s]]]: \*\*\*

\*1\*

instruction w

instruction x

instruction y

instruction z

\_1\_

|| SCIL at L1

|| Introductory boundary

|| Terminal boundary

uf00 [diff[$InfoXXYZ-[cs-s|sc-s], $InfoXXYZ-[cs-s|sc-s]]]: \*\*\*

\*1\*

instruction w

instruction x

instruction y

instruction z

\_1\_